Call for papers

IPSJ Transactions on System and LSI Design Methodology

- Objective
  - ✓ Widely publish research results on System LSI Design Methodology
- Scope

  Areas of interest include, but are not limited to:
  - ✓ **System Design Technology** (specification description, performance estimation, testing and verification, high level synthesis, model-based design, HW-SW co-design, design support/environment systems)
  - ✓**LSI Design Technology** (simulation and modeling, layout design, functional/logic synthesis, LSI testing and verification, high-reliable design, low power design, computer-aided design/design environment tools, emerging design technology)
  - ✓ **Design Experience** (embedded systems, reconfigurable systems, cyber-physical systems, AI-related technology, IoT/network applications, medical/healthcare applications, security applications, in-car systems, education for system/LSI design)
- About the Transactions
  - ✓ Issued twice a year on February and June
  - ✓ Submission deadlines are June and October, respectively

    (Next deadline is October 4, 2023 (hard deadline))

    *) From this year, previous December submission deadline (for next August issue) is to be changed to October (for next June issue).
  - ✓ All papers are available for free on the Web *http://www.jstage.jst.go.jp/browse/ipsjtsldm*
  - ✓ Papers are registered in many databases *INSPEC, EI, DBLP, SwetsWise, SciVerse Scopus, CrossRef, Google Scholar, Microsoft Academic Search, JDreamII*
  - ✓ Short paper category suitable for preliminary publication

    A paper with novelty OR useful information is considered for short paper publication.
    A complete version can be submitted later as a regular paper.
    A short paper is limited to 2 pages except for acknowledgement and references, and the revised version is within a half page more.
  - ✓ Invited papers by world-famous researchers
    - 32nd issue (Scheduled February 2024): Tadahiro Kuroda (The University of Tokyo), Scheduled to appear. Stay tuned !!
    - 26th issue (February 2021): Youngsoo Shin (KAIST), *"Computational Lithography Using Machine Learning Models"*
    - 25th issue (August 2020): Sheldon Tan, Zeyu Sun, Sheriff Sadiqbatcha (University of California at Riverside), *"Interconnect Electromigration Modeling and Analysis for Nanometer ICs: From Physics to Full-Chip"*
  - ✓ TSLDM Best Paper Award honors the authors of distinguished paper
- For more details, please visit ***http://www.sig-sldm.org/tsldm/***